Through-core capacitor array

What is a capacitor network?

This Product Selection Guide contains information to help select products in the Capacitor Networks,Arrays category on DigiKey.com Capacitor networks or arrays are devices of two or more capacitors in a single surface,through-hole or chassis mount package. The capacitors may be isolated from each other or connected in a bussed circuit type.

How much space does a discrete chip capacitor save?

Space savings can be quite dramatic when compared to the use of discrete chip capacitors. As an example,the 0508 4-element array offers a space reduction of >40%vs. 4 x 0402 discrete capacitors and of >70% vs. 4 x 0603 discrete capacitors. (This calculation is dependent on the spacing of the discrete components.)

What is a 0508 4-element capacitor array?

This in turn generates the opportunity to increase overall production output without further investment in new equipment. The 0508 4-element capacitor array gives a PCB space saving of over 40% vs four 0402 discretes and over 70% vs four 0603 discrete capacitors.

What is the difference between 0508 & 0612 capacitors?

The 0508 4-element capacitor array gives a PCB space saving of over 40% vs four 0402 discretes and over 70% vs four 0603 discrete capacitors. The 0612 4-element capacitor array gives a PCB space saving of over 50% vs four 0603 discretes and over 70% vs four 0805 discrete capacitors.

What is the difference between a 0612 vs 0603 capacitor array?

The 0612 4-element capacitor array gives a PCB space saving of over 50%vs four 0603 discretes and over 70% vs four 0805 discrete capacitors. KYOCERA AVX is the market leader in the development and manufacture of capacitor arrays.

What is a feedthru capacitor?

The unique construction of a feedthru capacitor provides low parallel inductance and offers excellent decoupling capability for all high di/dt environments and provides significant noise reduction in digital circuits to <5 GHz. A large range of capacitor values are available in either NP0 or X7R ceramic dielectrics.

Chen, NC, Chou, PY, Graeb, H & Lin, P-H 2017, High-density MOM capacitor array with novel mortise-tenon structure for low-power SAR ADC.? Proceedings of the 2017 Design, ...

There are presently several techniques for forming a buried capacitor in the core of a multilayer board. For purposes of this discussion, attention will be directed toward a sheet capacitor; ...

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mount package. The capacitors may be isolated from each ...

Compared with traditional chip capacitors, through-hole capacitors have the characteristic of connecting to the circuit board through the core leads. This connection ...

Capacitor Arrays DiaaEldin Khalil*, Mohamed Dessouky*, Vincent Bourguet**, Marie-Minerve Louerat**, ... The first part of the circuit is a switched-capacitor biquad. Through the ...

KEMET automotive grade array capacitors meet the demanding Automotive Electronics Council"s AEC-Q200 qualification requirements. Surface Mount Multilayer Ceramic ...

ADC with an optimized 5 C5 C6 segmented capacitor array. The lower 10 bits of the capacitor array are all composed of unit capacitors without any calibration unit. Without calibration, the ...

In this paper the Through Silicon Capacitor (or TSC), a new type of decoupling capacitor integrated on a via bridge silicon interposer is presented. TSC is a tri-dimensional ...

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In this analysis, the time to discharge a buried capacitor innerlayer through a 13 mil through hole via and a 5 mil blind via is calculated for a particular board design. Amplitude Decay 0.1 0 0.5 ...

The circuit, implemented in a 130 nm process, consists of a 16 Kb array with a configuration of 128 × 128. To minimize the impact of parasitic capacitance on the sampling capacitor, a ...

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